

Some (Post-)Exascale Challenges The NumPEx Program and InPEx Project Contributions

KeyNote 2 - Long-term Computing Vision Feb 13 2024, 2024 ETP4HPC Conference

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15 years ago, Simulating The Future'08 One million core programming workshop



Some international Exascale efforts ...

Search

IESP 2008, ...2012



HOME MEETINGS DOCUMENTS COMMUNITY

DOCUMENTS

CONTENTS

1 Versions of the Roadmap 1.1 Current Version of the IESP Roadmap 1.2 Previous Versions of the IESP Roadmap

2 Products of Recent Meetings 2.1 Products of the Cologne Meeting 2.2 Products of the San Francisco Meeting

3 Most Recent: Meeting 8 (Kobe, Japan, April 12-13, 2012) 3.1 Day 1: Morning Plenary Presentations 3.2 Day 1: Afternoon Plenary Presentations 3.3 Day 1: Afternoon Breakouts 3.4 Day 2: Morning Plenary Presentations 3.5 Day 2: Reports from afternoon breakouts 3.6 Documents of Interest Nowing from this meeting

4 Presentations at the IESP exploratory meeting SC08, Austin, TX, 18Nov08

5 Meeting 1 (Santa Fe, NM, USA) 5.1 Presentations 5.1.1 Summaries from the meeting breakout groups 5.1.2 Pienary Presentations 5.2 Whitepapers

6 Meeting 2 (Paris, France) 6 1 Full Workshop Report

BDEC 2013, 2014 ... 2020



The Basis for BDEC

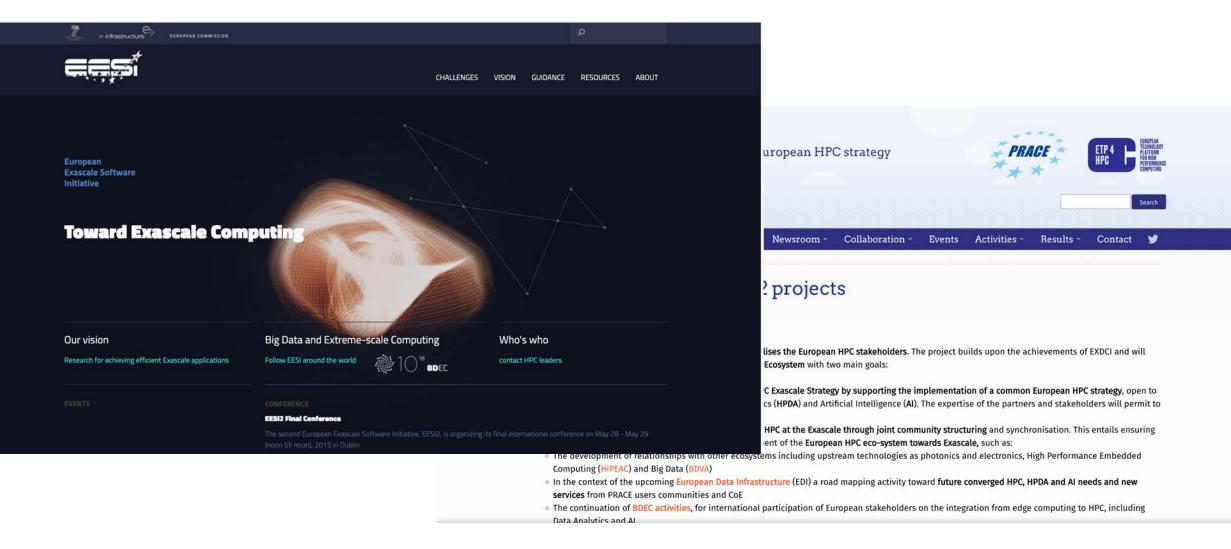
Informed partly by the work of the International Exascale Software Project (IESP, 2009-2012), in the past five years, the United States, the European Union, Japa China have each moved aggressively to develop their own plans for achieving exascale computing in the next decade. Such concerted planning by the tradition high performance computing speaks eloquently about both the substantial rewards that await the success of such efforts, and about the unprecedented technic that apparently block the path upward to get there.

But while these exascale initiatives have understandably focused on the big challenges of exascale for hardware and software architecture, the relatively recent of the phenomena of Big Data in a wide variety of scientific fields represents a tectonic shift that is transforming the entire research landscape on which all pla exascale computing must play out.

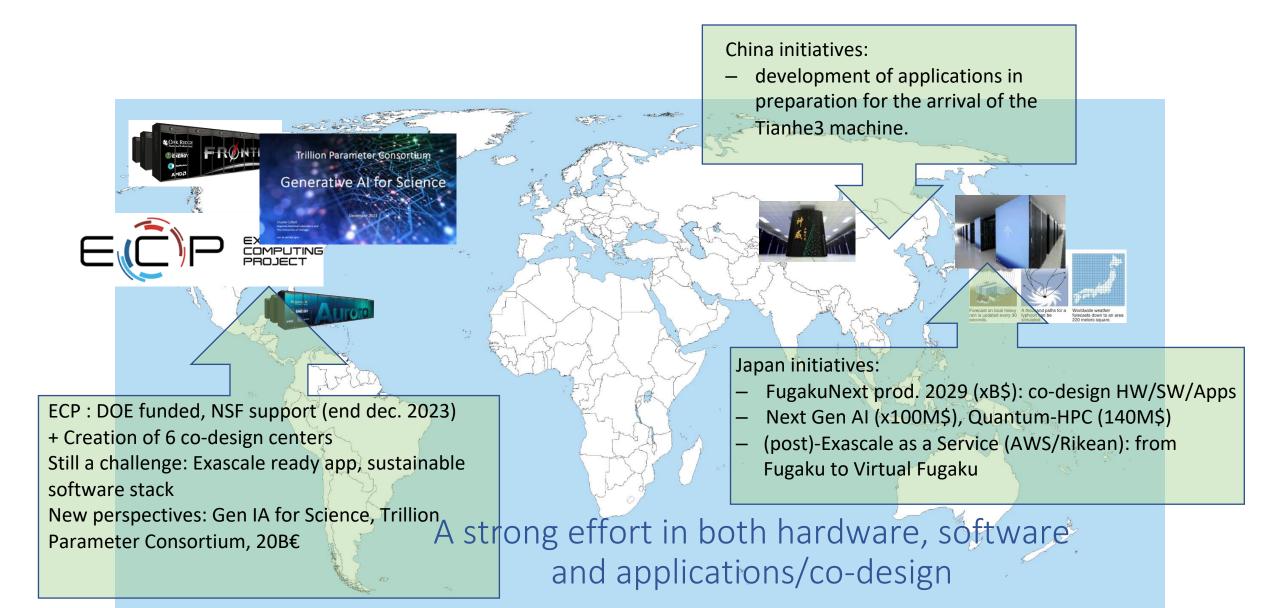
Building on the work of the IESP, the Big Data and Extreme-scale Computing (BDEC) community has staged a series of workshops that have endeavored to map account for the ways in which the major issues associated with Big Data intersect with, impinge upon, and potentially change, the national (and international) are now being laid for achieving exascale computing. Some international Exascale efforts ...

With coordinated European contribution (CSA)

EESI 1&2, EXDCI 1&2 2010, ...2020



The (post)-Exascale race, where are we?



The (post)-Exascale race, where are we?



The International Post-Exascale (InPEx) Project

What to share and coordinate...

- Al for Science, HPC for Al Datasets for Al training
- Software Exascale stack production, management and sustainability :
 - Packaging, documentation, builds, results, catalogs, continuous integration, Linux builds (E4S, etc.)
- Digital continuum and data management
- Co-design (HW&SW&Apps), benchmarks and evaluation
- Math and algorithm for Exascale
- Energy and environmental impact and sustainability
- Future and disruptive technologies and usage (investment
- Training



2/13/24

The International Post-Exascale (InPEx) Project

InPEx expected outcomes

- Landmark documents largely exploited, worldwide, for supporting future postexascale science
- Contribute to the implementation of an international, shared, high-quality computing environment based on the principles and practices of co-design
- Formation of a solid network of exascale computing leaders, all around the globe **Actions**:
- Dedicated international working groups
- International Post-Exascale (InPEx) workshop series

Participants:

Researchers, engineers, industry, funding bodies



The International Post-Exascale (InPEx) Project

Inpex.science

Date	(10/2023)	11/2023	06/2024	06/2025
Location	Preparatory phase EU (France)	SC'23 - BOF	Workshop1 EU/BSC	Workshop2 Japan
Date	03/2026	09/2026	06/2027	09/2027
Location	Workshop3 US	Workshop4 EU	Workshop5 Japan	Workshop6 US

How to contribute? Send 2 pages white paper to : inpex@inpex.science



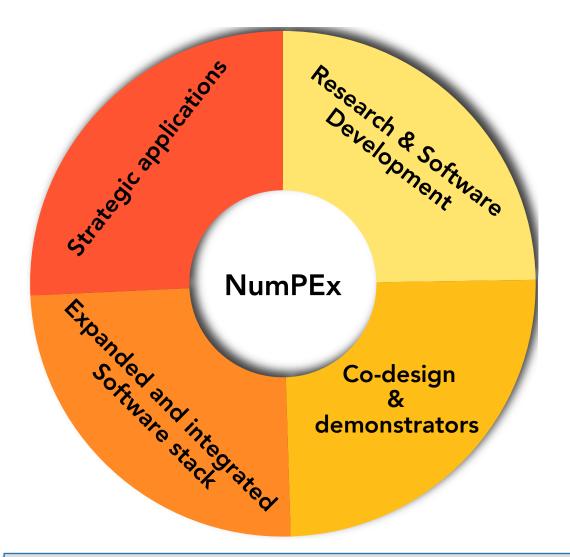
The French NumPEx Program

Consolidating and accelerating the construction of a sovereign European exascale software stack and strategic applications exascale capability in a coherent and multi-annuel framework

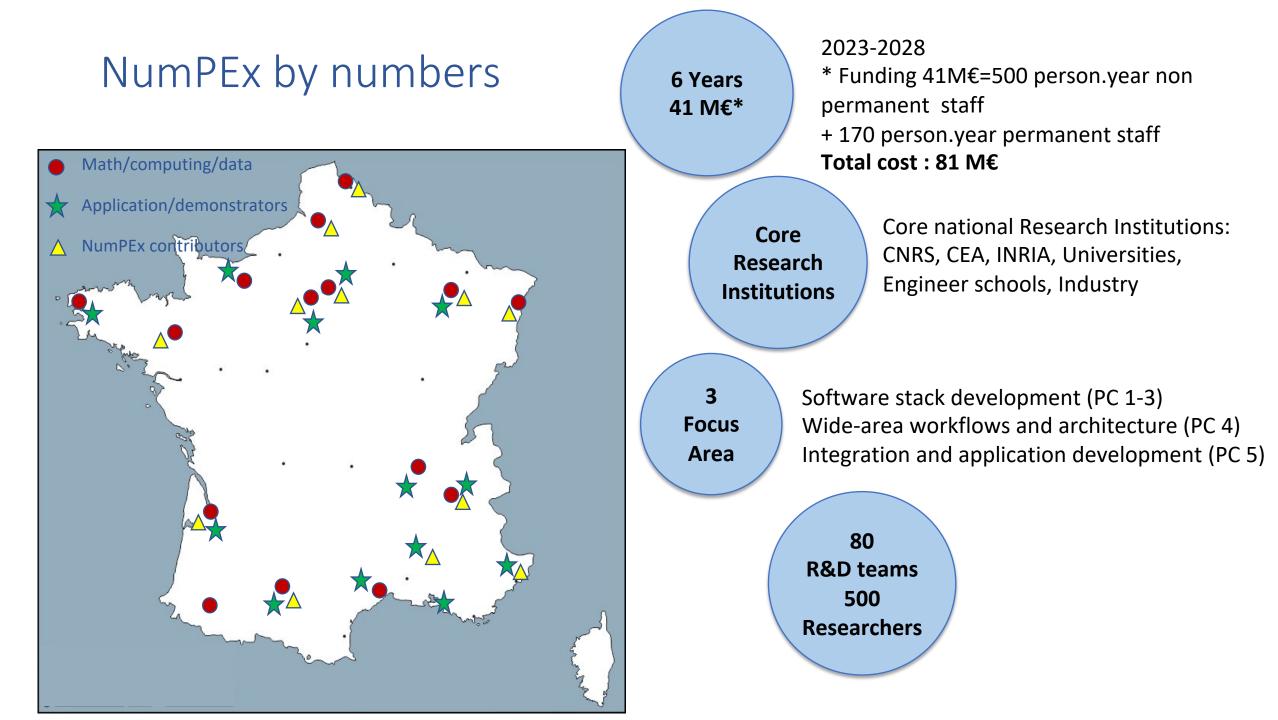
Integrate and validate **co-designed** innovative methods, libraries and software stack with demonstrators of strategic applications.

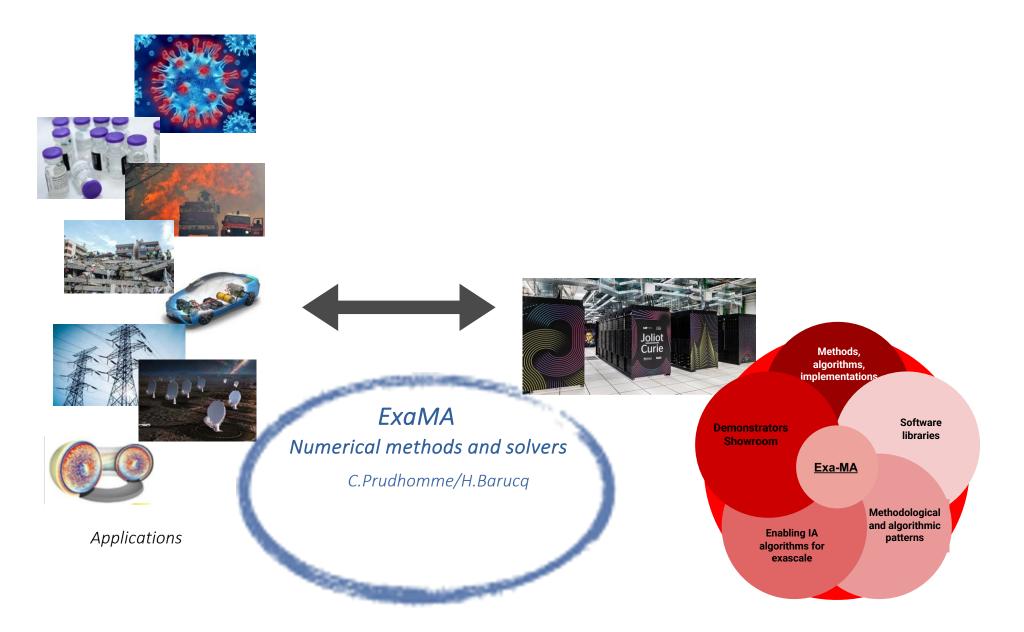
Accelerate science-driven and engineering-driven developers training and software productivity

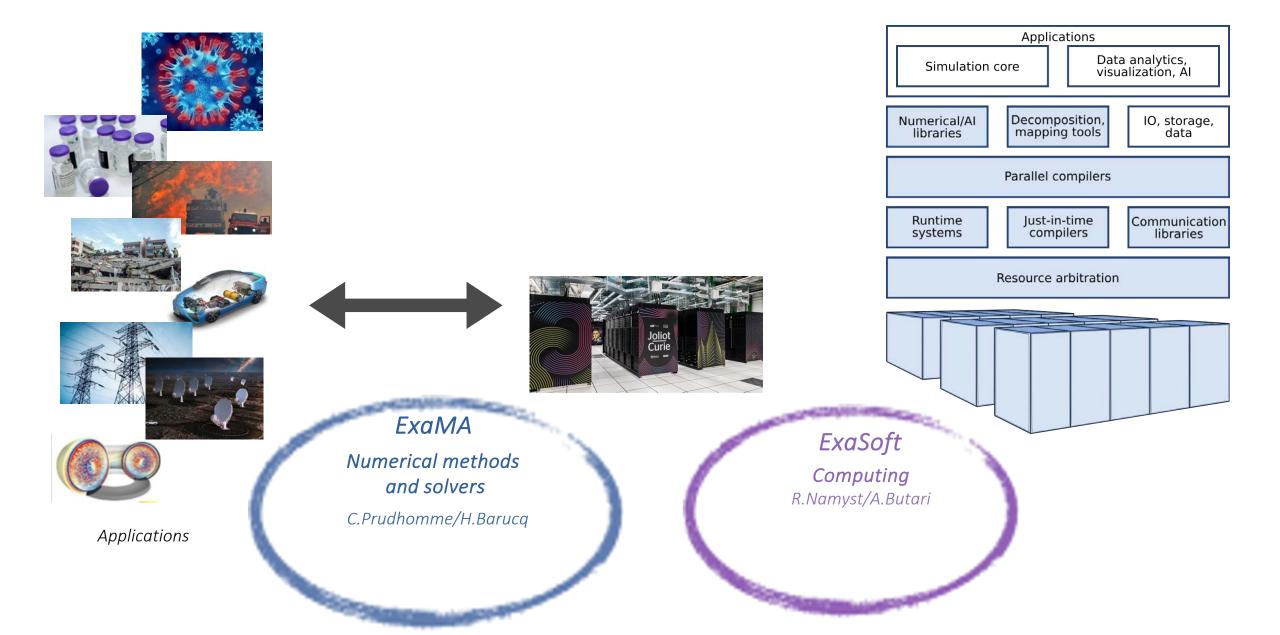
Foster national and international collaborations to prepare for the Exascale and post-Exascale era



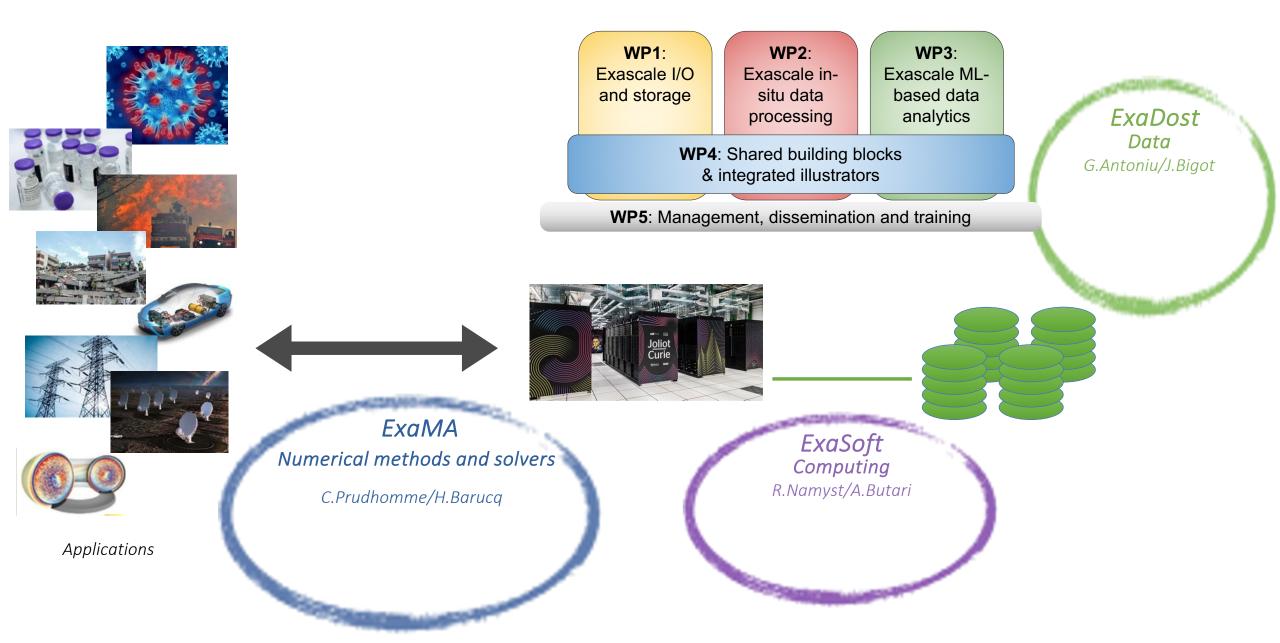
Help aggregate the French Edge/Cloud/HPC/HPDA/IA community

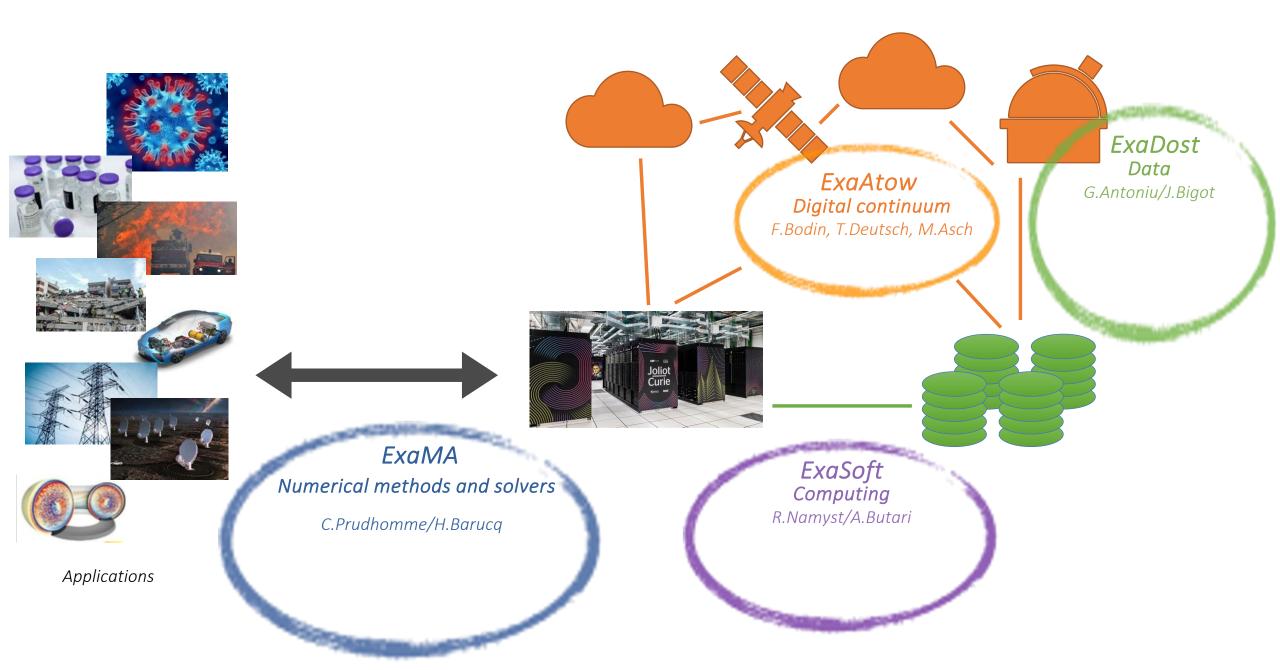


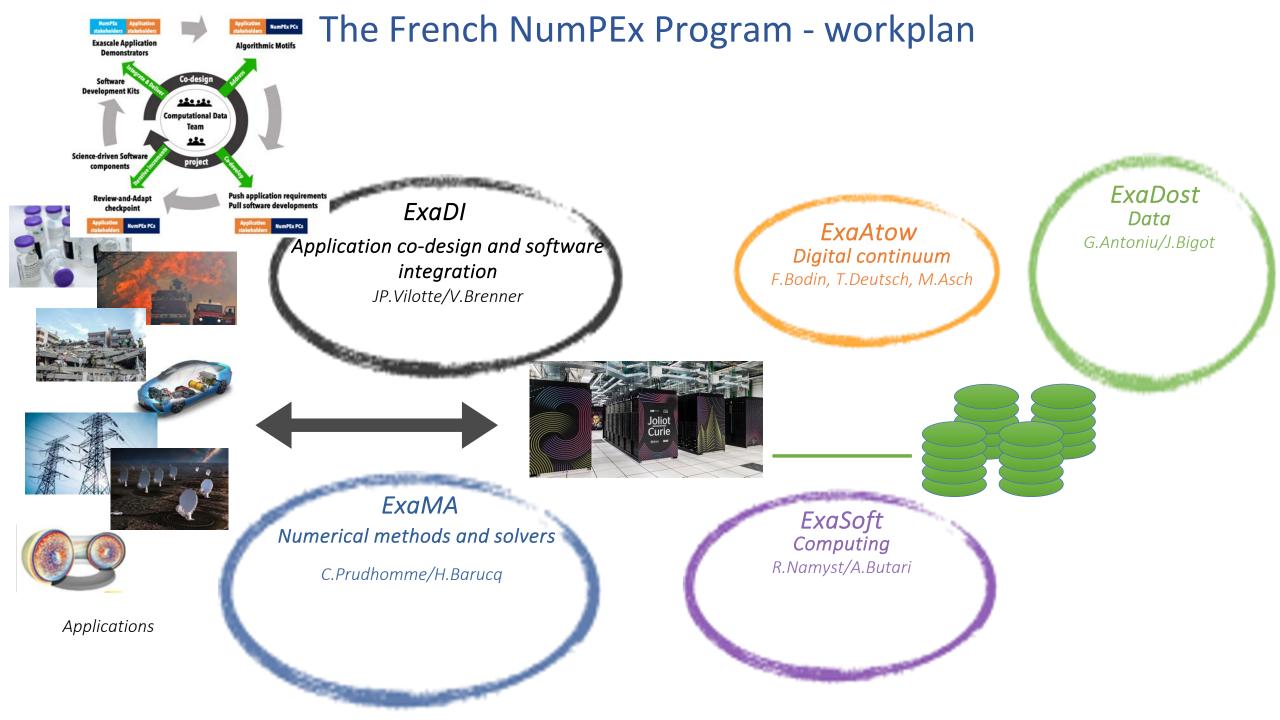






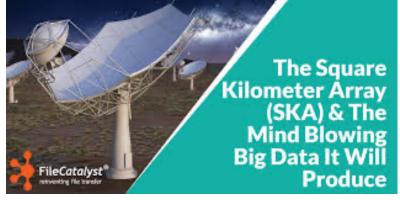




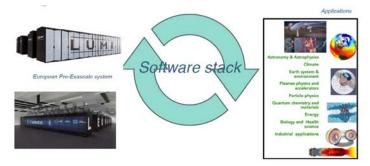


Focus on some post-exascale challenges

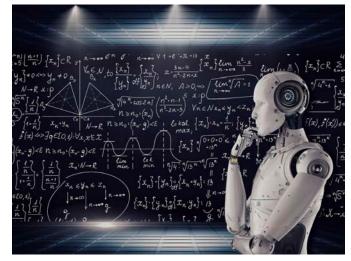
From edge to HPC systems The digital continuum



Software/application co-design



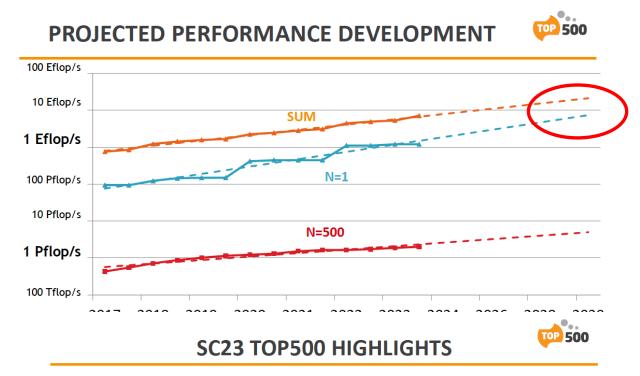
AI4Science – Science4AI



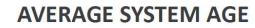
Software, the new frontier



Software, the new frontier

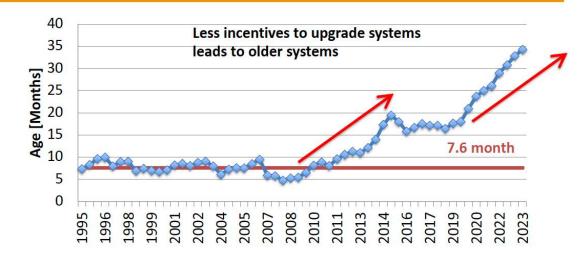


- Frontier is #1 and remains only ExaScale system in the TOP500 for now
 - 4 new systems in TOP10! + one upgrade
 - And 9 additional new systems in TOP50
- First Intel Sapphire CPUs (25) and GPUs (4) on the list
- HPC systems are used longer and replaced less often
 - Due to an increasing number of technological limits
 - Leads to strong concentration at the top (research as well as commercial)
- TOP500 shows new reduced growth-rates since 2017!
 - End of original Moore's Law scaling
 - Unlikely to achieve 10 Exascale by the end of the decade unless we fundamentally change "business"!





SOFTWARE



Software, the new frontier



Software is the new power

- The race for power is slowing down: we're aiming for 10 Exaflops in 2030, not 1000
- => Importance of Math&software developpment to gain effective power: new digital schema, precision mix, innovative discretization methods, energy aware algorithms, ...
- Capitalize on and consolidate software production in Europe and worldwide
- Importance of software sustainability
- Offer an "industrial" production environment converging towards "As a Service".
- Broaden the potential user base towards new AI/HPC/HPDA/Cloud/Edge uses
- Enable and facilitate, or even make possible, what was not possible before, through the coupling of components produced in different contexts (e.g. Kokkos/StarPU/HWLOC) and thus enable the emergence of new services, uses and capacities to move towards exascale

At stake: consolidating and accelerating the construction of a **sovereign European exascale software stack**

Software, the new frontier

Software Integration challenge

Context:

- HPC applications and machines are gaining in complexity (think compute continuum, HPC/HPDA/DL hybridization), leading to high costs to build and deploy applications on supercomputers, impairing portability and reproducibility.
- The standard software installation process on supercomputers is reaching its limits module load + cmake/make install

At Stake:

• Need for a HPC dev-ops methodology for fast deployment while ensuring performance and supporting reproducibility efforts

NUMPEX Strategy: Modern Software Packaging (SPACK and GUIX)

- Have users work with package managers to capture software dependencies with compilation process to automate deployment.
 - Can generate containers or be used directly if installed on supercomputer. guix pack -f docker mysoft guix install mysoft
 - Can also be used by system admin to generate modules





The Edge-Cloud-HPC continuum

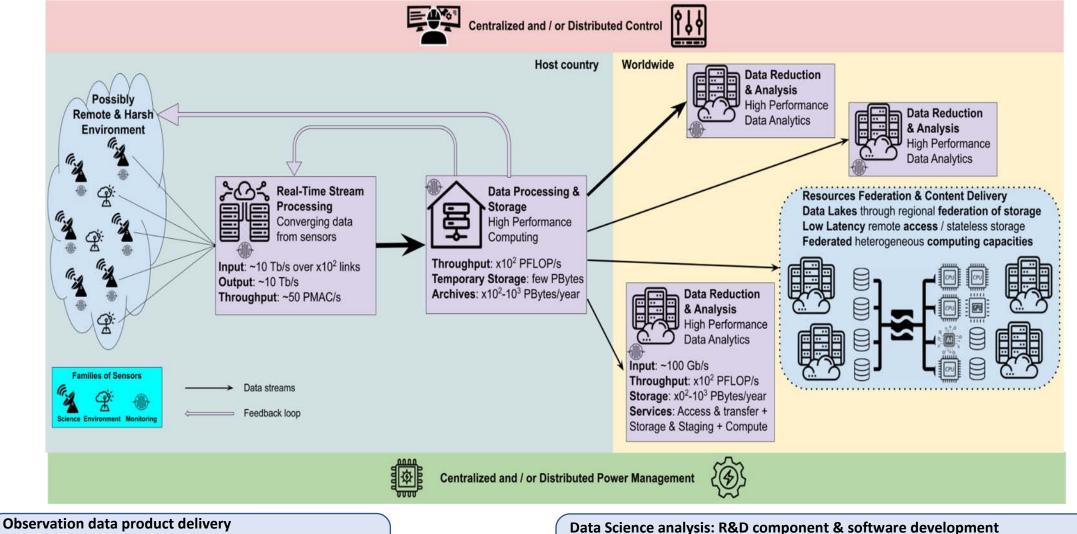


Distributed workflows across edge, cloud and HPC systems



- TCI TransContinuum Initiative
- The Exascale machine is part of a "datasphere"
 - Data from large scientific instruments
 - Data from sensors (IoT)
 - Data from simulations
- The primary challenge: developing integrated software ecosystems across this continuum
 - At an extreme scale with stringent cybersecurity constraints

The SKA data workflow from sensors to HPC centers



- •Data streaming reduction and processing,
- •Edge computing and content delivery network

Data Science analysis: R&D component & software development
Emerging technologies compute, I/O, storage, wide area workflows
Distributed & heterogeneous architecture, content delivery network
Power management

The digital continuum: open challenges

Unification of HPC Simulations/Big Data/AI towards a data-centric view

Moving, storing and processing data across the continuum: how to deal with the 3 Vs of Big Data?

Extreme Volume across the continuum

 Support the access and processing of "cold", historical data and "hot", real-time data + (virtually infinite) simulated data

• Extreme Velocity across the continuum

- Unified real-time data processing (in situ/in transit, stream-based) in a common software ecosystem
- Need disruptive reduction in data movement cost with new devices, packaging
 - All real-time data may not be storable in archives => real time training (bandwidth-oriented)

• Extreme Variety across the continuum

- Unified data storage abstractions to enable distributed processing and analytics across the continuum
- Interoperable data formats, "Semantic interoperability" through shared ontologies
- Digital Continuum is a multi-tenant and multi-owner environment.
 - Collected data used with multiple purposes
 - Computing Infrastructure is also shared

The Post-Exascale digital continuum: action plan

- Coordinate efforts to share workflows solutions and services for the convergence of AI/HPC/HPDA/Cloud/Edge
- Develop the concept of EaaS, Exascale as a Service, for Tier-O European systems
- Contribute to the development of a data-everywhere, FAIR, ecosystem in Europe
- Identifying new emerging usages including urgent computing and digital twins



Al everywhere, a game-changer at the post-Exascale era

- Al at all stages from numerical methods to workflows, etc.
- Trend towards Al-centric HW, which also puts huge constraints on the HPC SW stack

International initiatives have started, Europe must stay in the race

- US AI program, Trillion Parameter Initiative (<u>www.tpc.dev</u>).
- Initiatives to produce extreme scale LLMs, AuroraGPT, FugakuGPT, ...
- Pushing for European initiatives, with a strong focus on Science

Challenges of HPC/AI convergence

- How to develop validated/robust/trustworthy AI models for Science
- In the post-Exascale era, data-centric era, should **benefit from Al-centric software**
- HPC/AI software stack convergence ?

AI for science – towards HPC/AI hybridization

• End of Moore's law, **develop hybrid approaches based on AI** to accelerate parts of scientific computing applications

=> 10 Exascale performance with x25 EDP+Data science pp and x42 HW improvements, some AI-based solvers can be sped up by 6 orders of magnitude, etc., weather forecast with Graphcast

- Hybridization of HPC SW with AI : physics-informed AI models for simulation codes, observational data reduction, digital twins.
- Push forward a post-Exascale-ready SW stack embedding AI solutions that answer the needs of the application communities



Al at scale at the post-Exascale era

- Rapid emergence of extremely large AI models (*e.g. LLM, multimodal GPT, diffusion models, etc*), which require huge compute capacity, massive storage.
- **Challenges in data management and computing** on Exascale/post-Exascale grade systems, with massive use of heterogeneous accelerated architecture.
- Push a post-Exascale SW stack that further covers AI-based libraries, with interfaces between traditional HPC librairies and standard AI/Machine Learning framework.

Accelerate AI for Science



- Accelerate the diffusion and application of AI at all stages, with a very strong focus on AI for scientific and engineering applications.
- Build upon an AI-centric co-design activity, focusing on **shared flexible open AIcentric application use-cases/mini-apps**, with curated data and well-defined application dependent metrics.

Fostering international initiatives and collaborations

 Foster Al-centric European initiatives, with a specific focus on enabling the development of Al at scale for Science, not only focused on large fondation models.

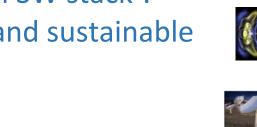
Software/application co-design

Key challenges

- How to get post-Exascale ready applications?
- How to expand an application-driven SW stack ?
- How to make applications portable and sustainable at the post-Exascale era?

International context

- Early-binding HW/SW/application co-design approach at Rikken (Japan)
- In the USA, DOE co-design centers were a key component of the Exascale Computing project (ECP)
- Inspired by ECP, co-design is central in the NumPEx project (FR)













Astronomy & Astrophysics Earth System Models & Climate

> Earth sciences & environment

Computational biology & Life science

Laboratory laser-plasma physics

High-energy particle physics

Quantum chemistry and materials

Digital health

Environmental & societal risks

Urban systems planing

Magnetically confined fusion plasma

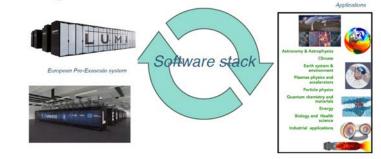
Sustainable Transport & mobility

> **Energy production &** transport



Software/application co-design

Help the applications get prepared for post-Exascale challenges



- Towards **shared and common mini-apps/proxy-apps**, centered about technical functionalities/bottlenecks (AMR, I/O, AI, etc.)
- Shared benchmarks are central to test/evaluate portability/performance/deployability on different HW.
- Towards a post-Exascale application-driven software stack
 - Application code development is a long and costly journey, greater reuse of composable/interoperable SW components
 - Long-term visibility/sustainable development for applications is key.

Take away messages



Software, the new frontier

Consolidating and accelerating the construction of a sovereign European **exascale software stack** (portable, interoperable, reproducible, sustainable)

Support and foster the developpement of disruptive Math & models



From edge to HPC system: the digital continuum

Coordinate efforts to share workflows, solutions and services for the convergence of HPC/Cloud/Edge

EaaS: Exascale as a Service, for Tier-0 European systems

Develop a data-everywhere, FAIR, ecosystem in Europe



AI4Science – Science4AI

Push an **hybrid AI/HPC software stack**, to accelerate HPC and provide AI at scale

Support AI for Science, foster fully open AI usecases/benchmarks, not restricted to GenAI



Software/application co-design

HW/SW/application co-design to help the communities get prepared for post-Exascale

Foster the use/reuse of modular/interoperable and portable SW components

Push sustainable SW development model $_{\rm 31}$